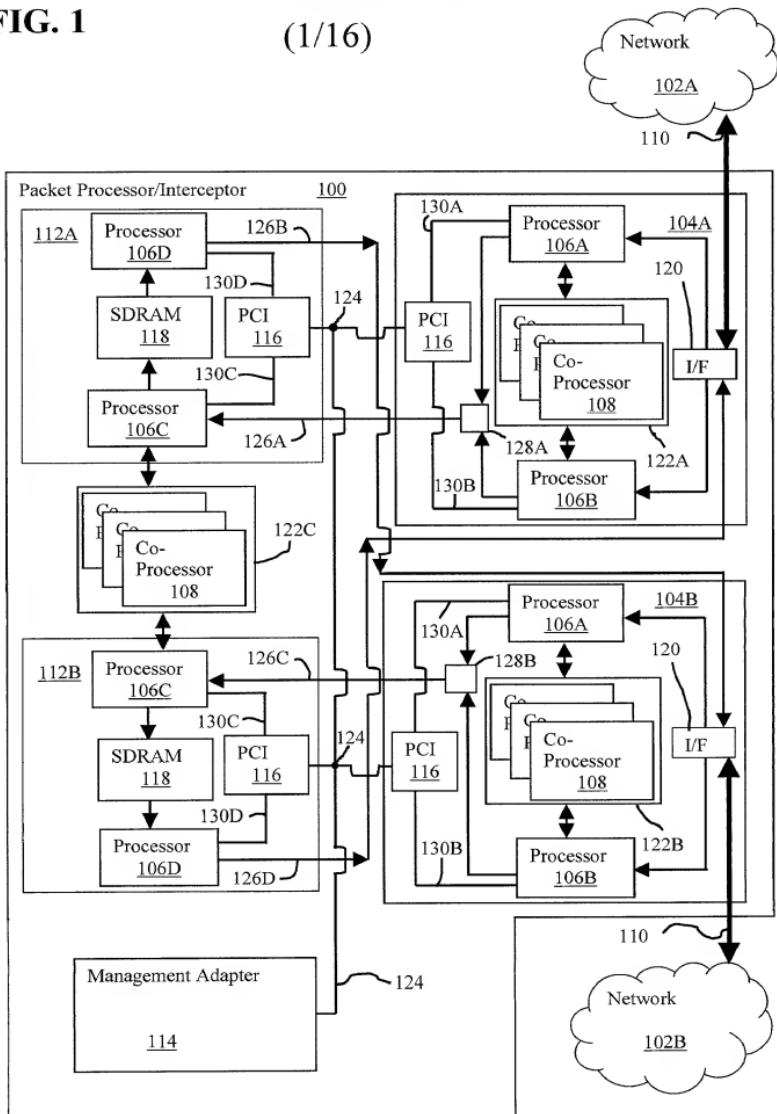


FIG. 1

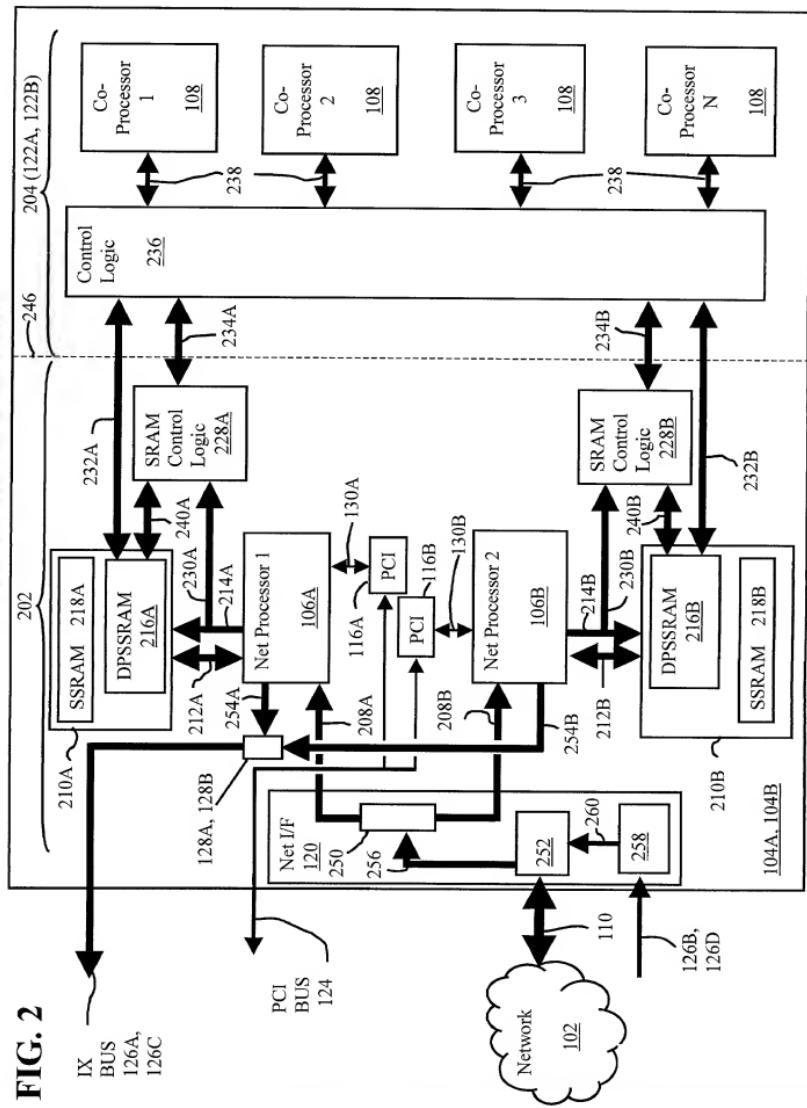
(1/16)



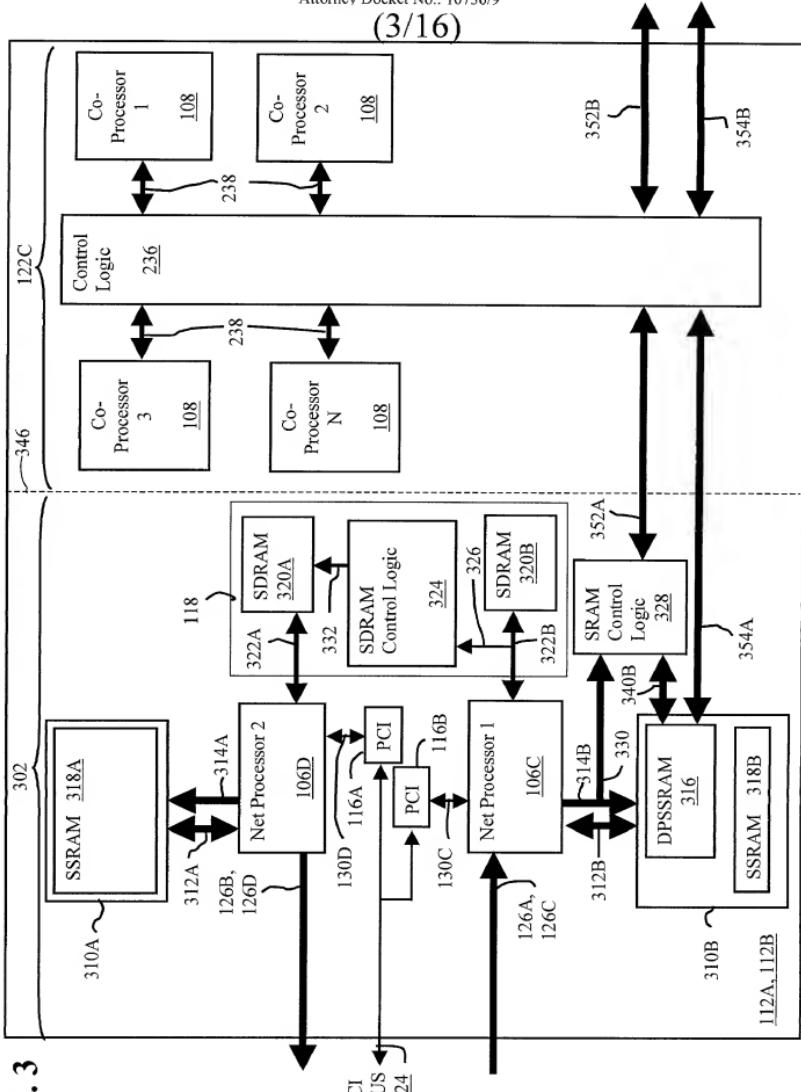
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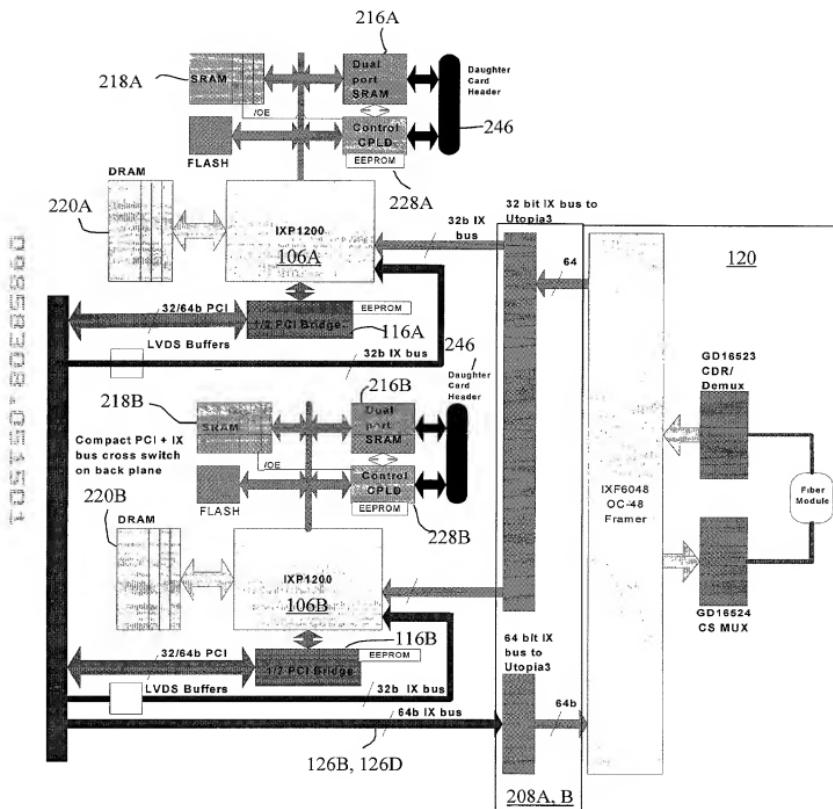


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## FIG. 4

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FIG. 5: FIGURE 5

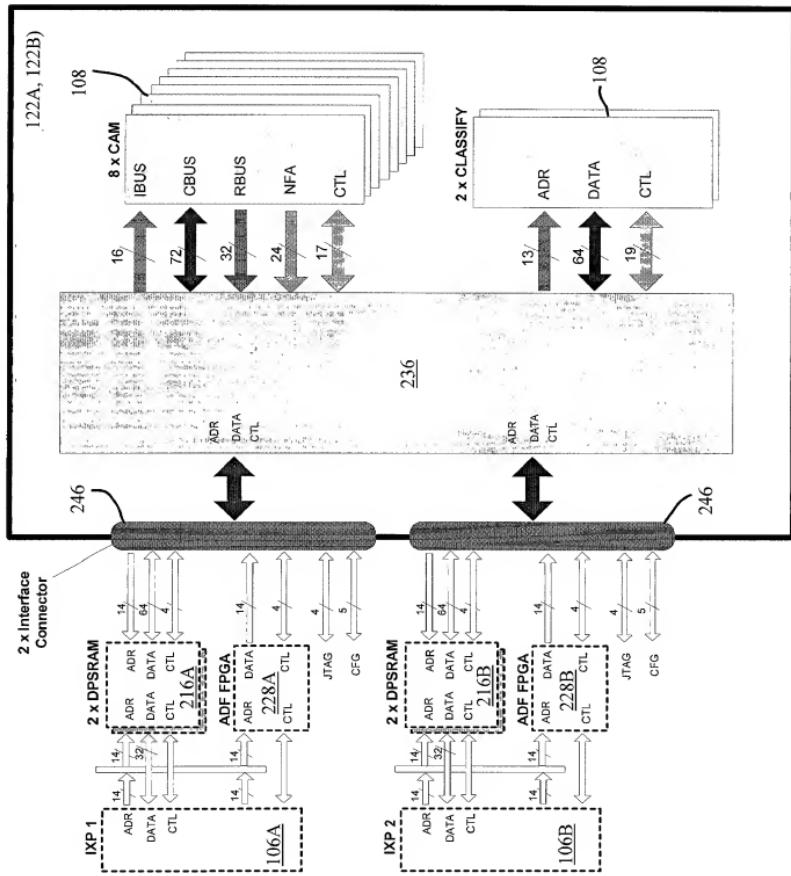


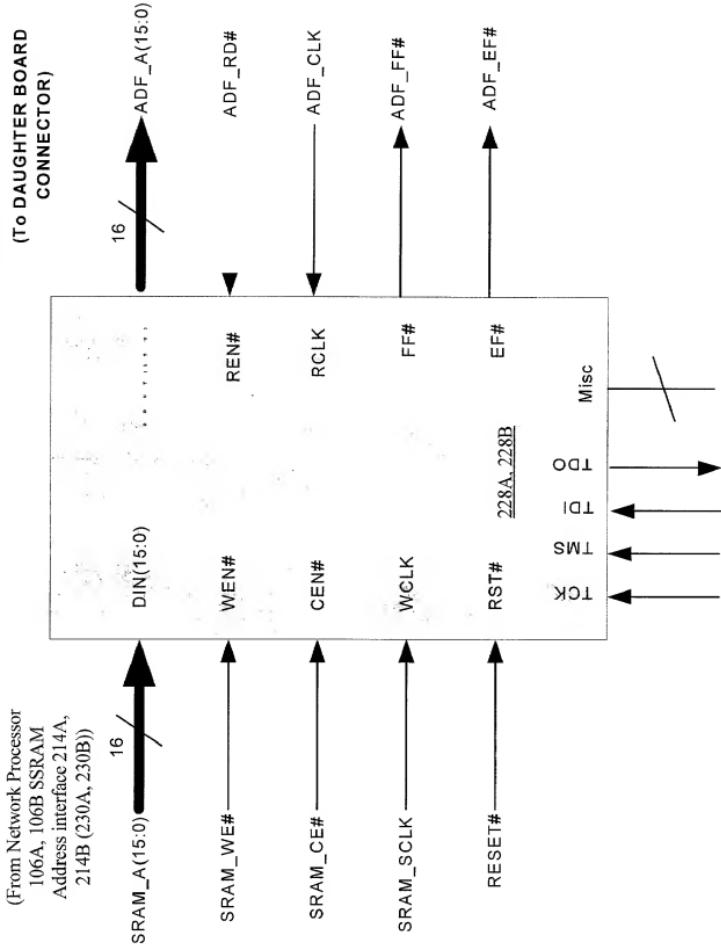
FIG. 5

Inventor(s): Zahid Najam, Peder J. Jungck, Andrew T. Nguyen

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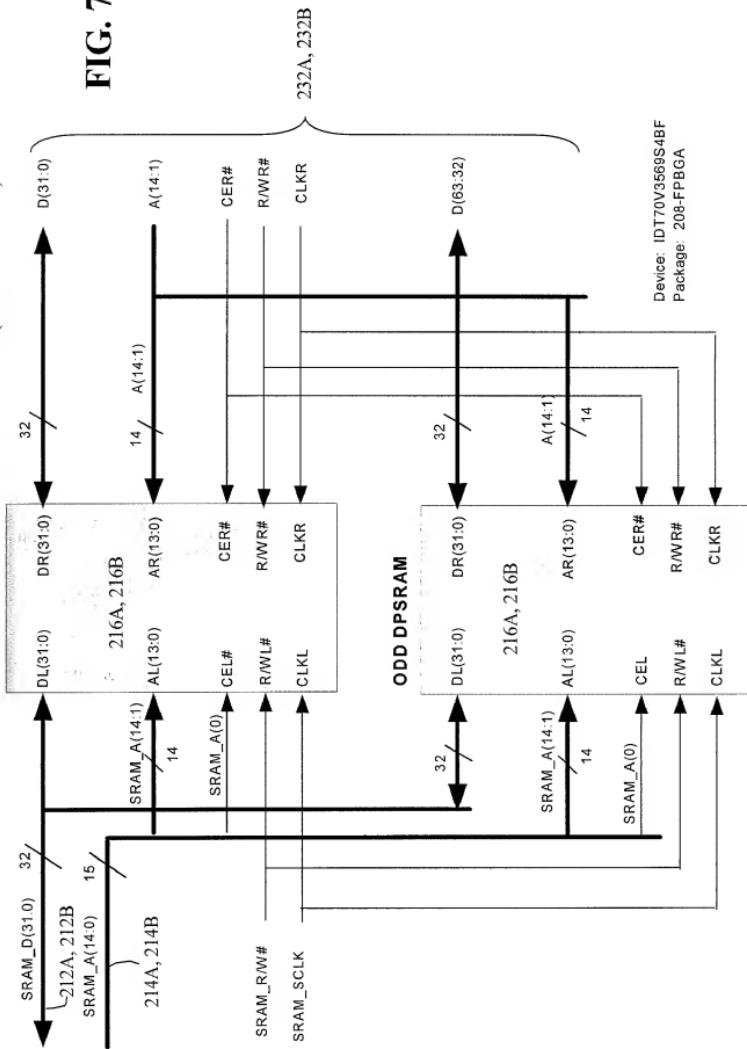
FIG. 6



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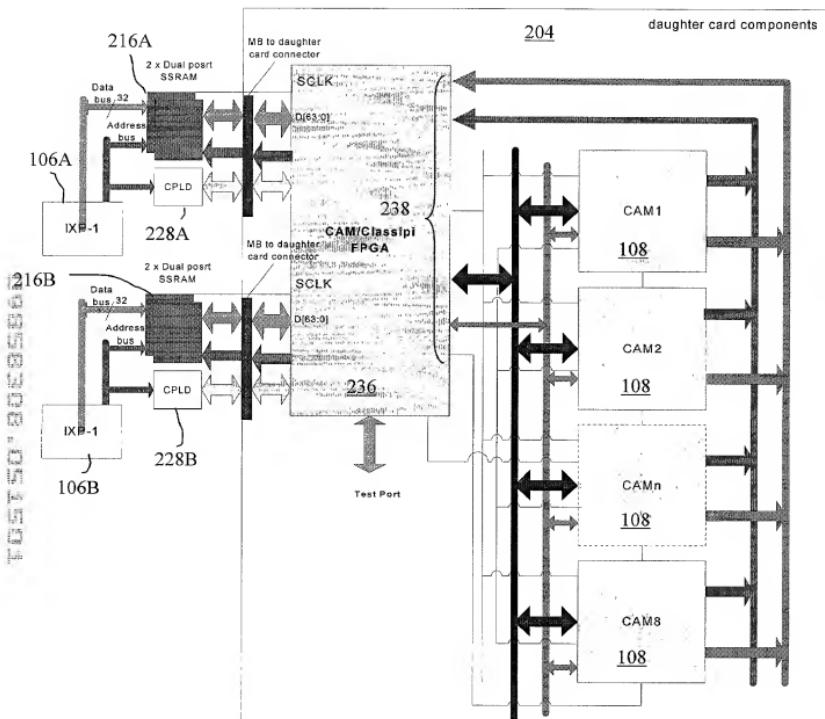
FIG. 7

(To Connector 246)

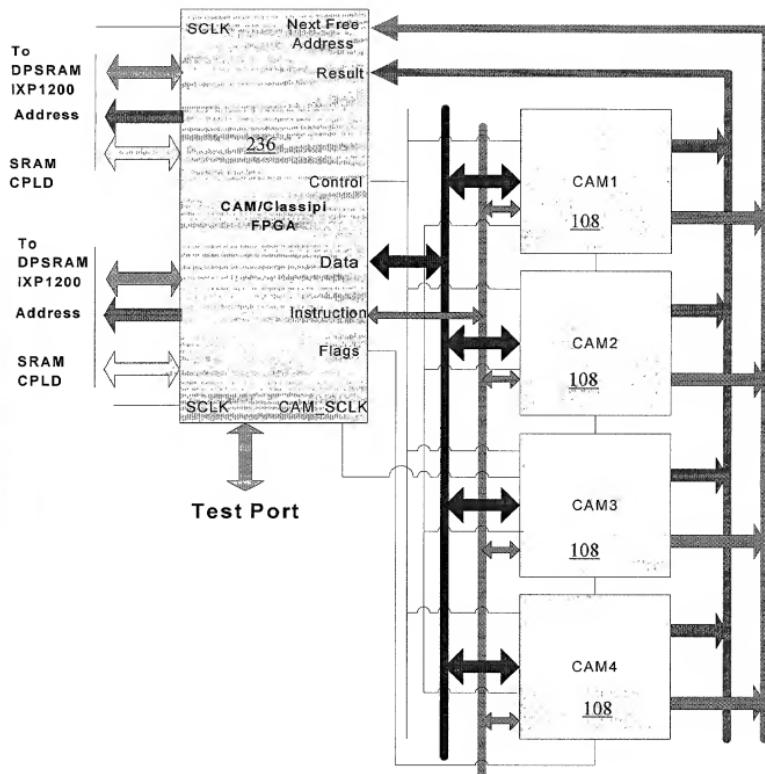


## FIG. 8

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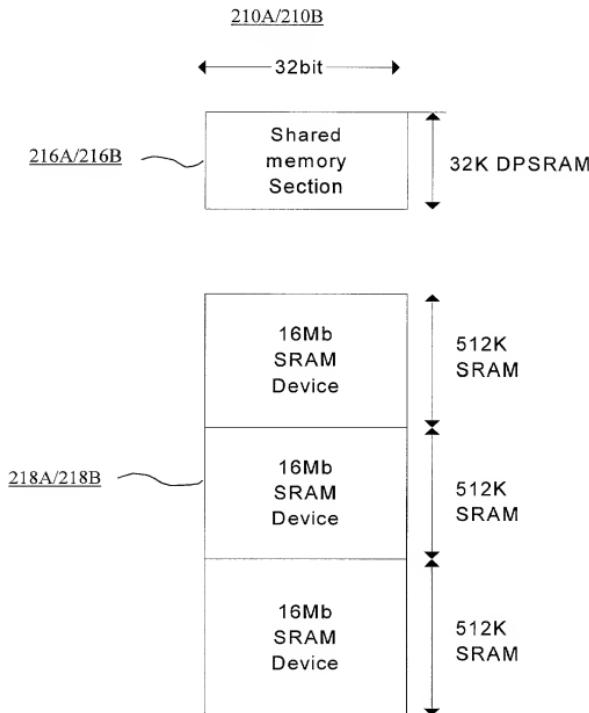


**FIG. 9**  
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## FIG. 10

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FIG. 11

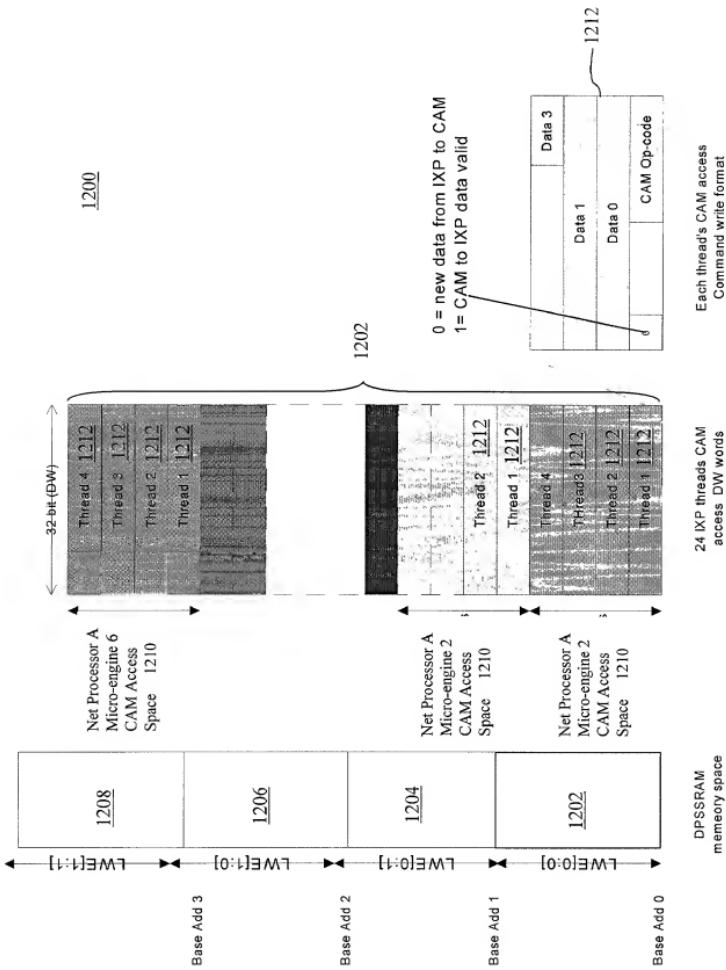
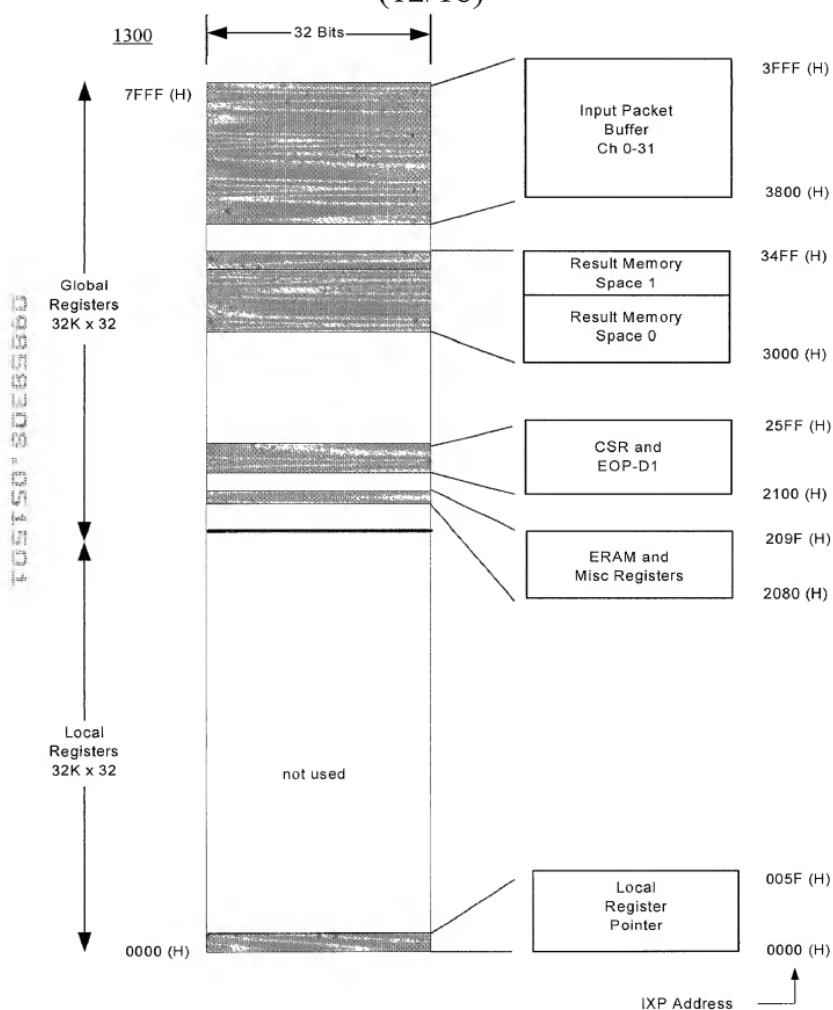
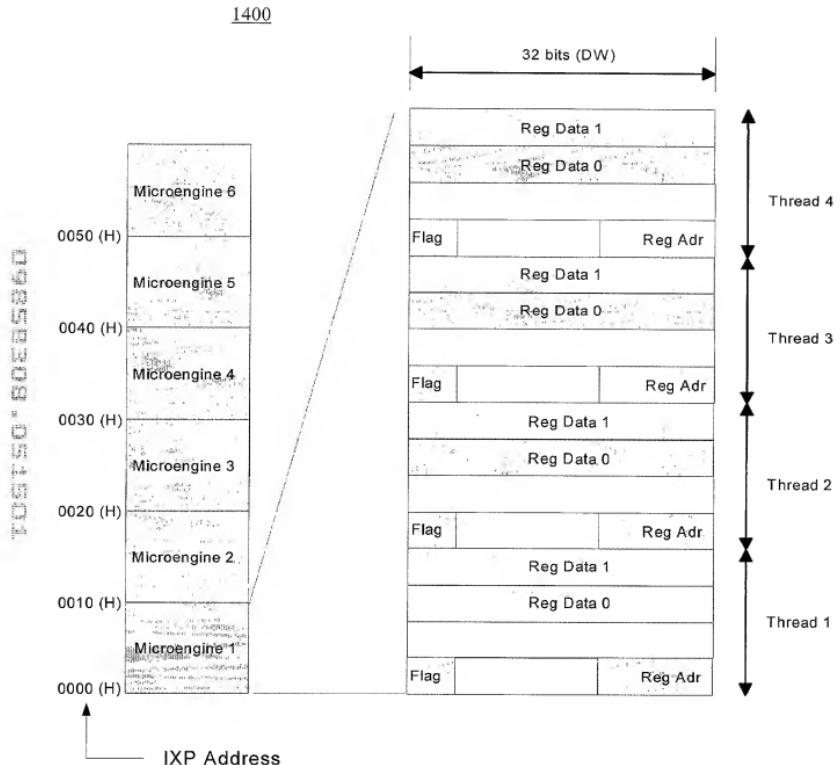


FIG. 12

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## FIG. 13 (13/18)



**FIG. 14**1500

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7FFF (H)

Channel 32

32 bits (DW)

Channel 4

Channel 3

Channel 2

Channel 1

7000 (H)

Byte 252	Byte 253	Byte 254	EOP
Byte 248	Byte 249	Byte 250	Byte 251

Data Byte 4	Data Byte 5	Data Byte 6	Data Byte 7
Data Byte 0	Data Byte 1	Data Byte 2	Data Byte 3

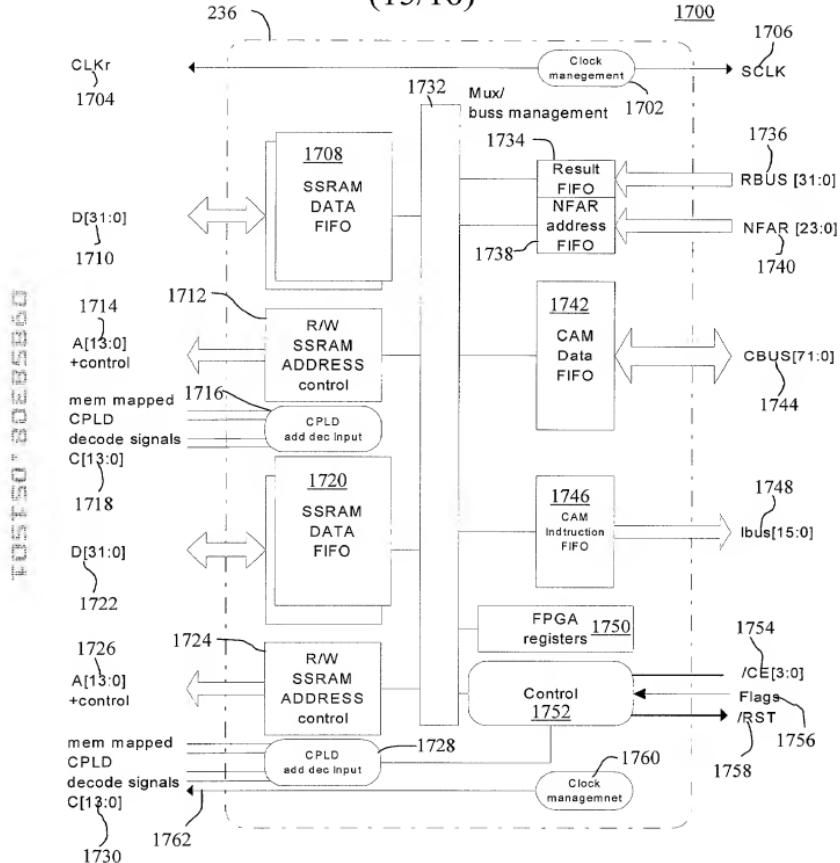
64 DW



IXP Address

FIG. 15

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FIG. 16

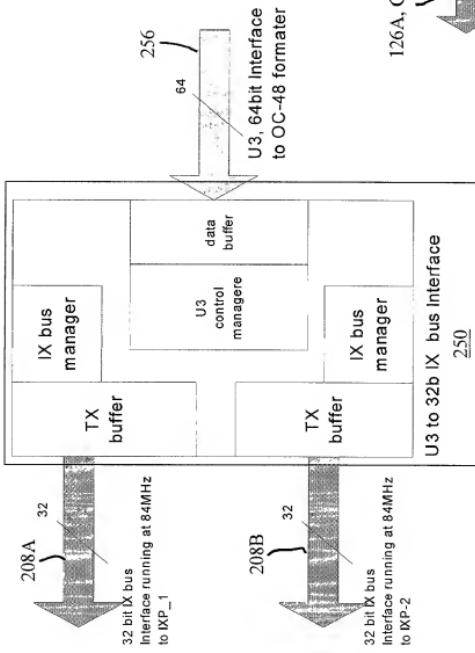


FIG. 17

